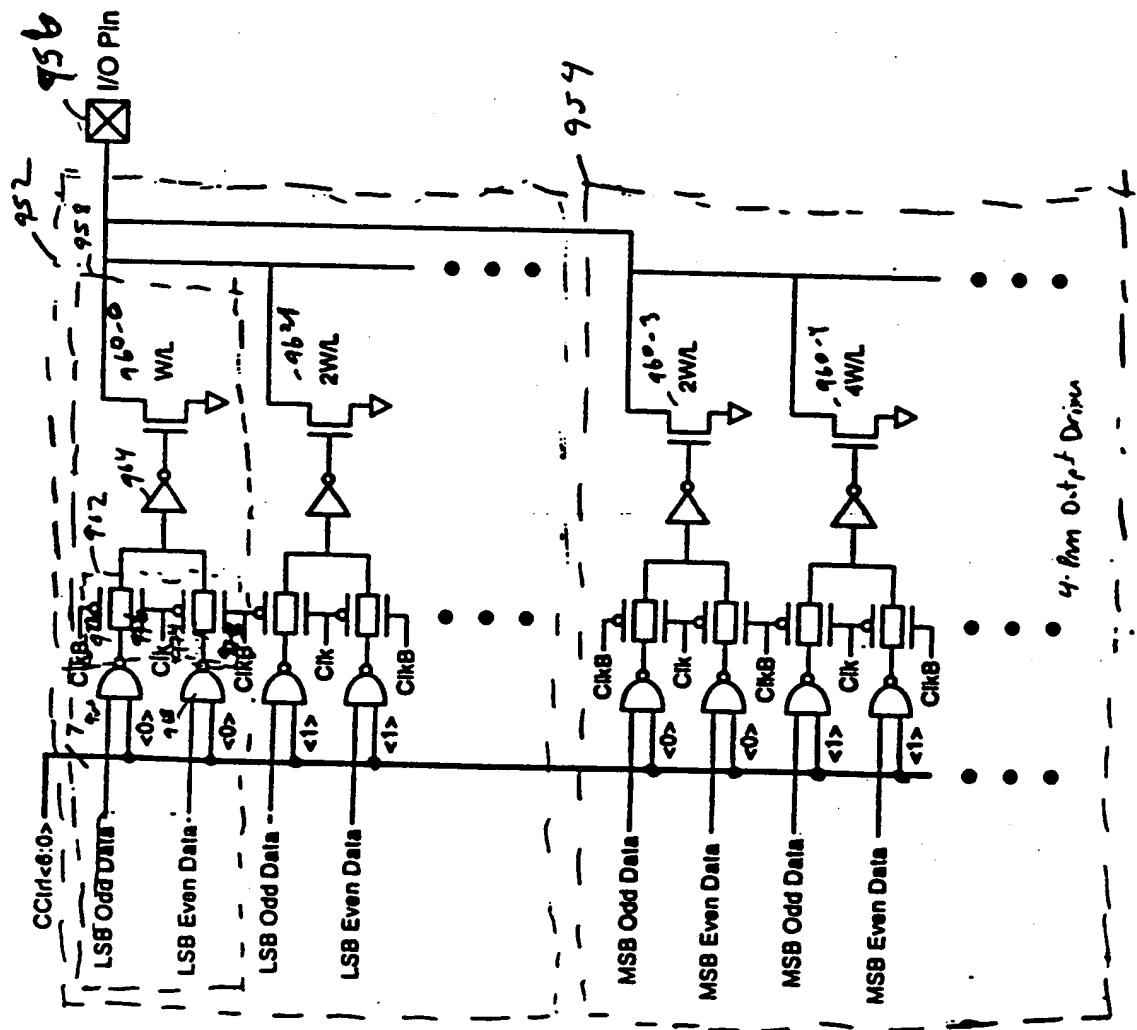


FIG. 2

esb ↗



416.

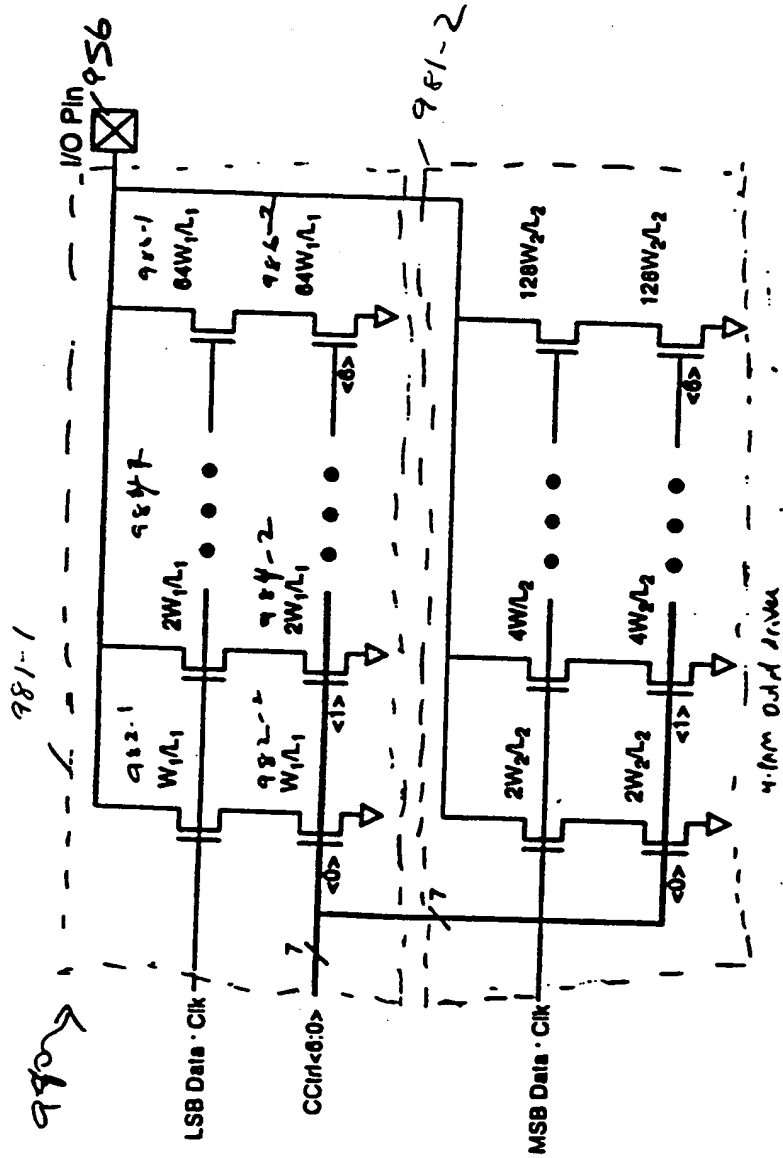


FIG 38

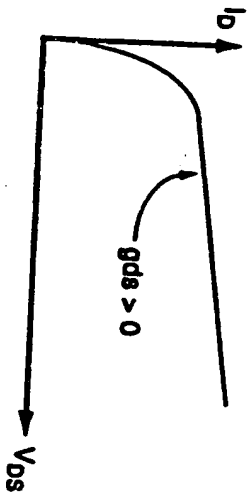


FIG. 4A

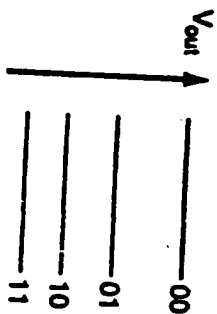


FIG. 4B

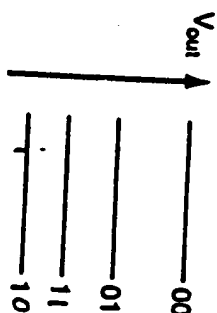
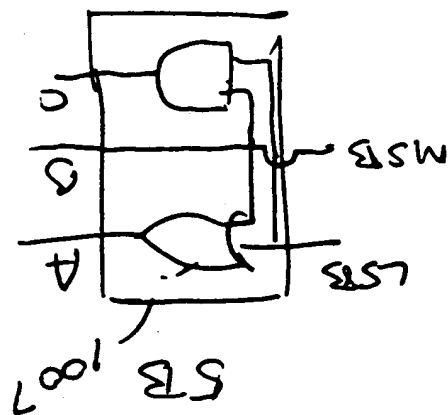
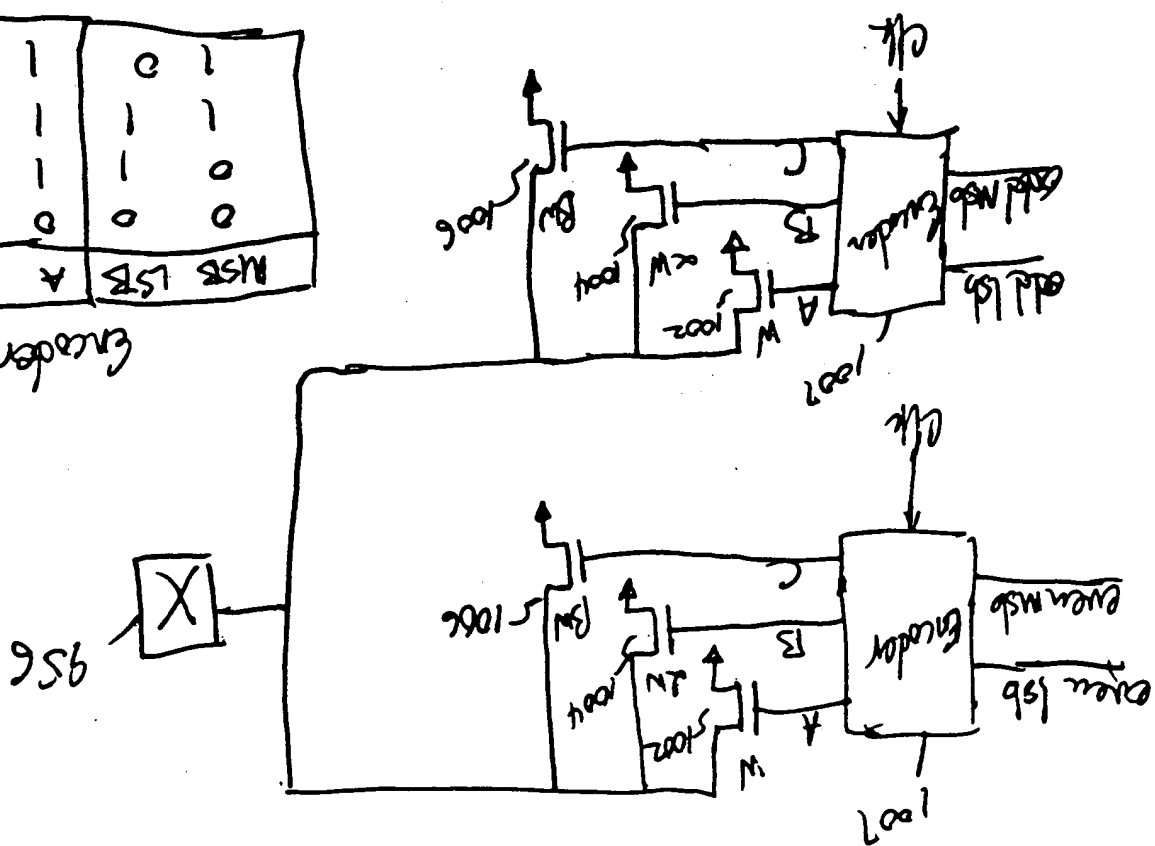


FIG. 4C

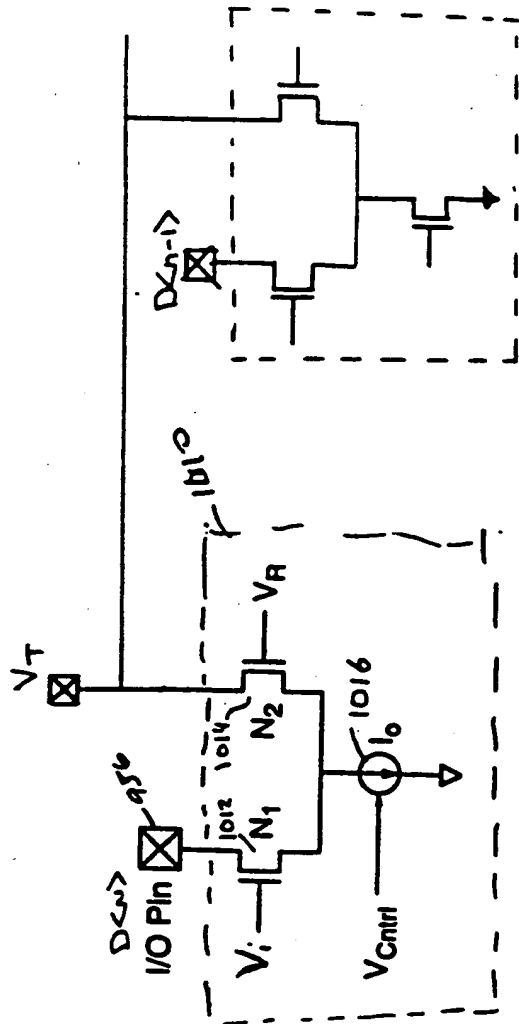


enclosed page



175 5A

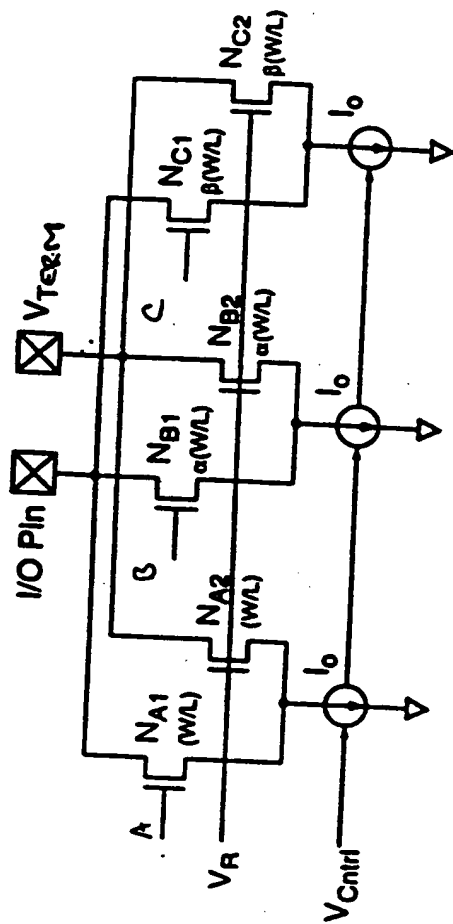
1001

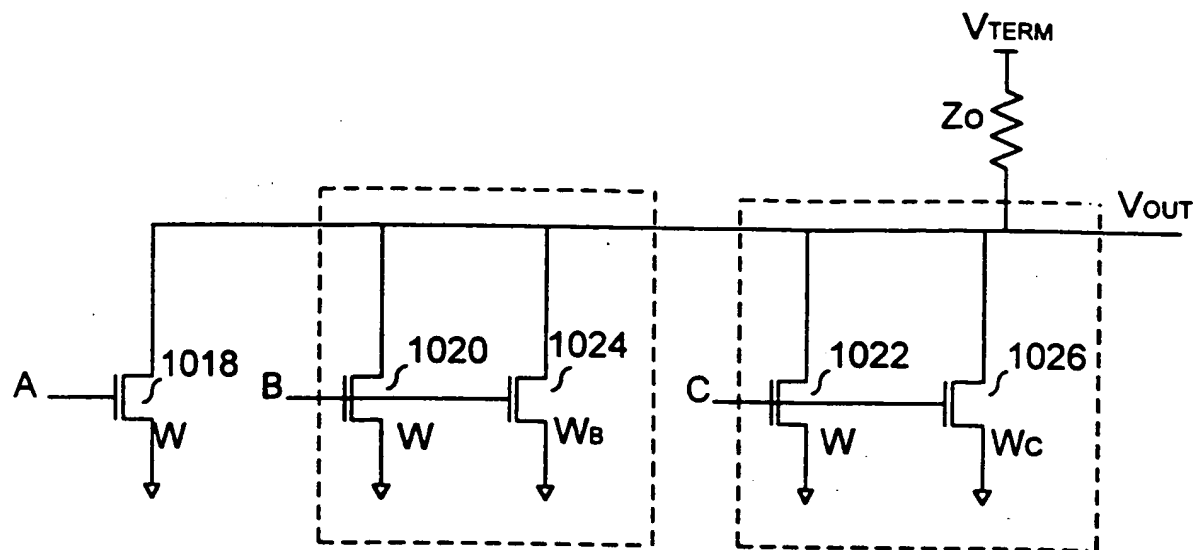


Circuit to Reduce Switching Noise

FIG

6

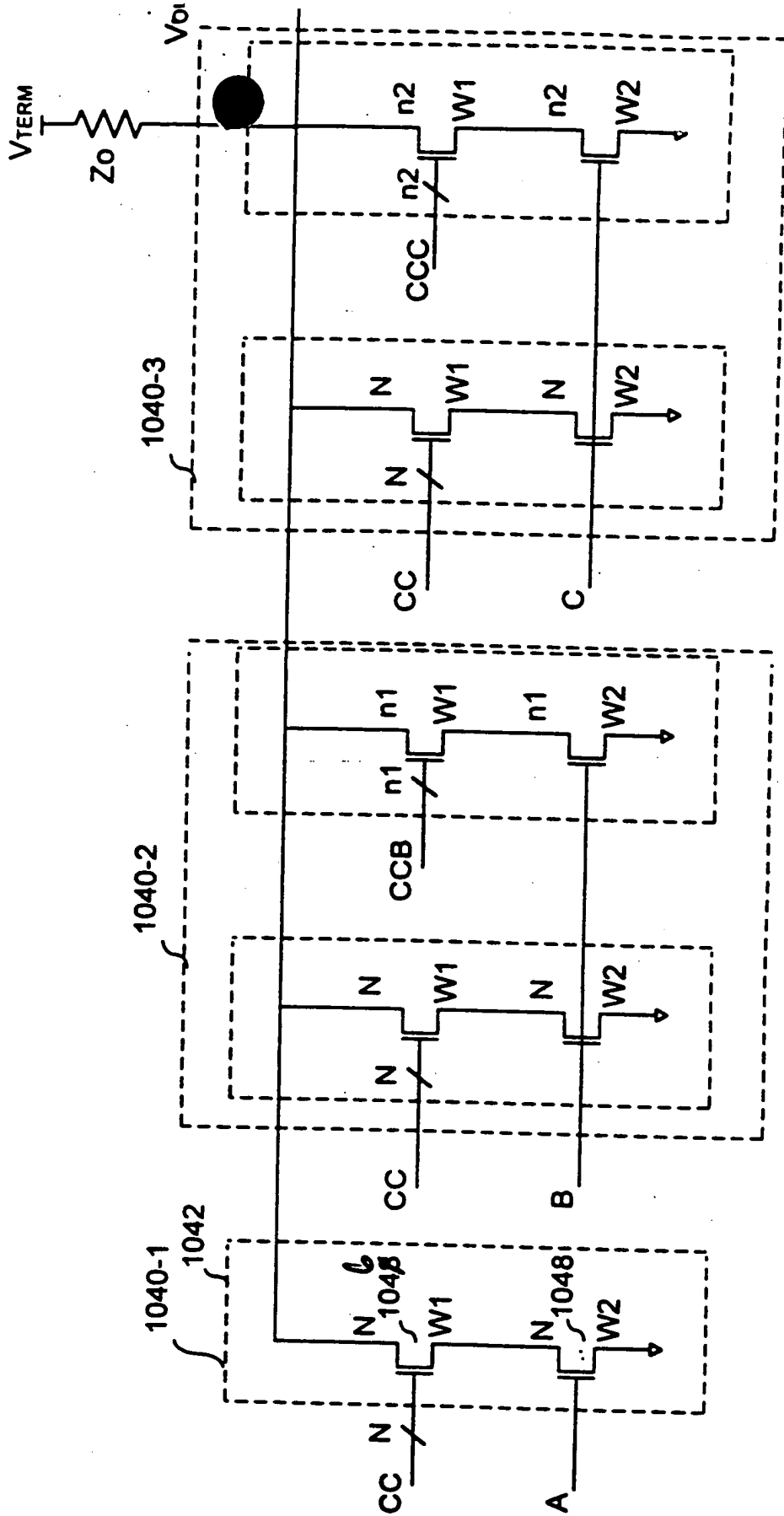




GDS Compensated Multi-PAM Output Driver

FIG.

8



GDS Compensated Multi-PAM Output Driver with Current Control
FIG. 9A

1042 ↗

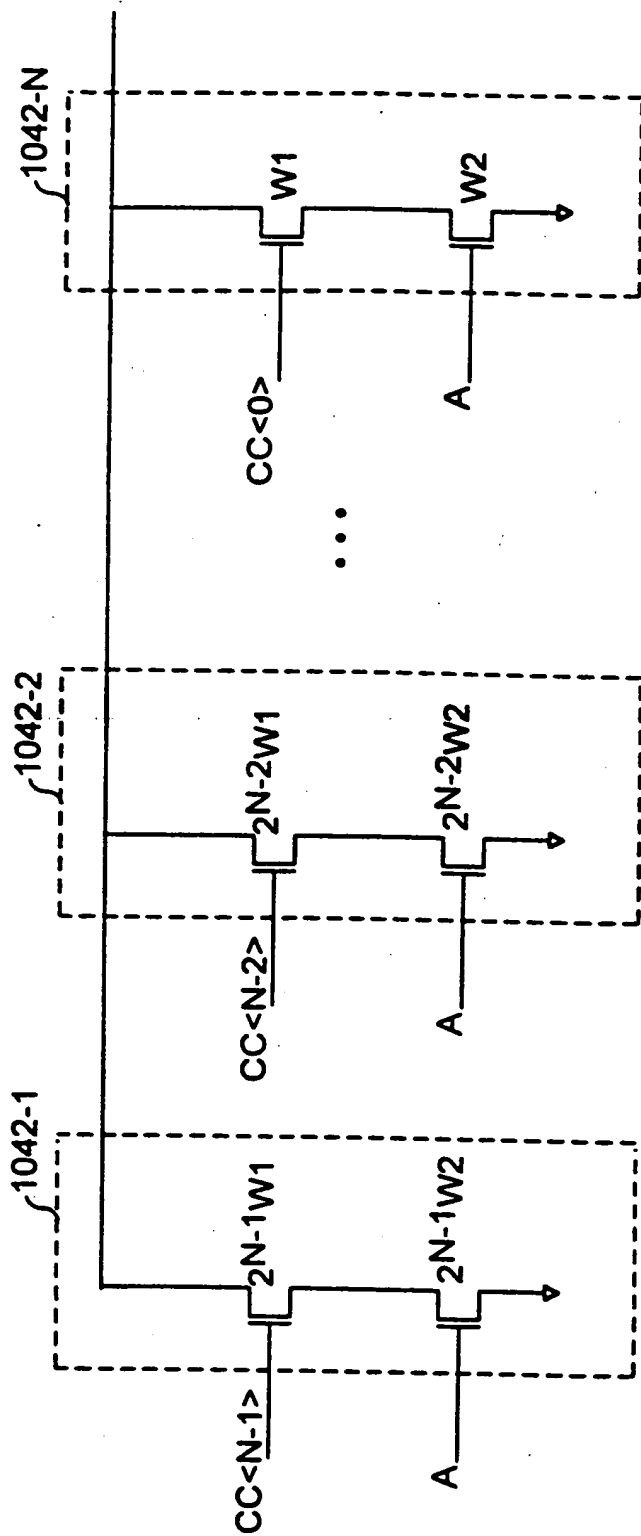
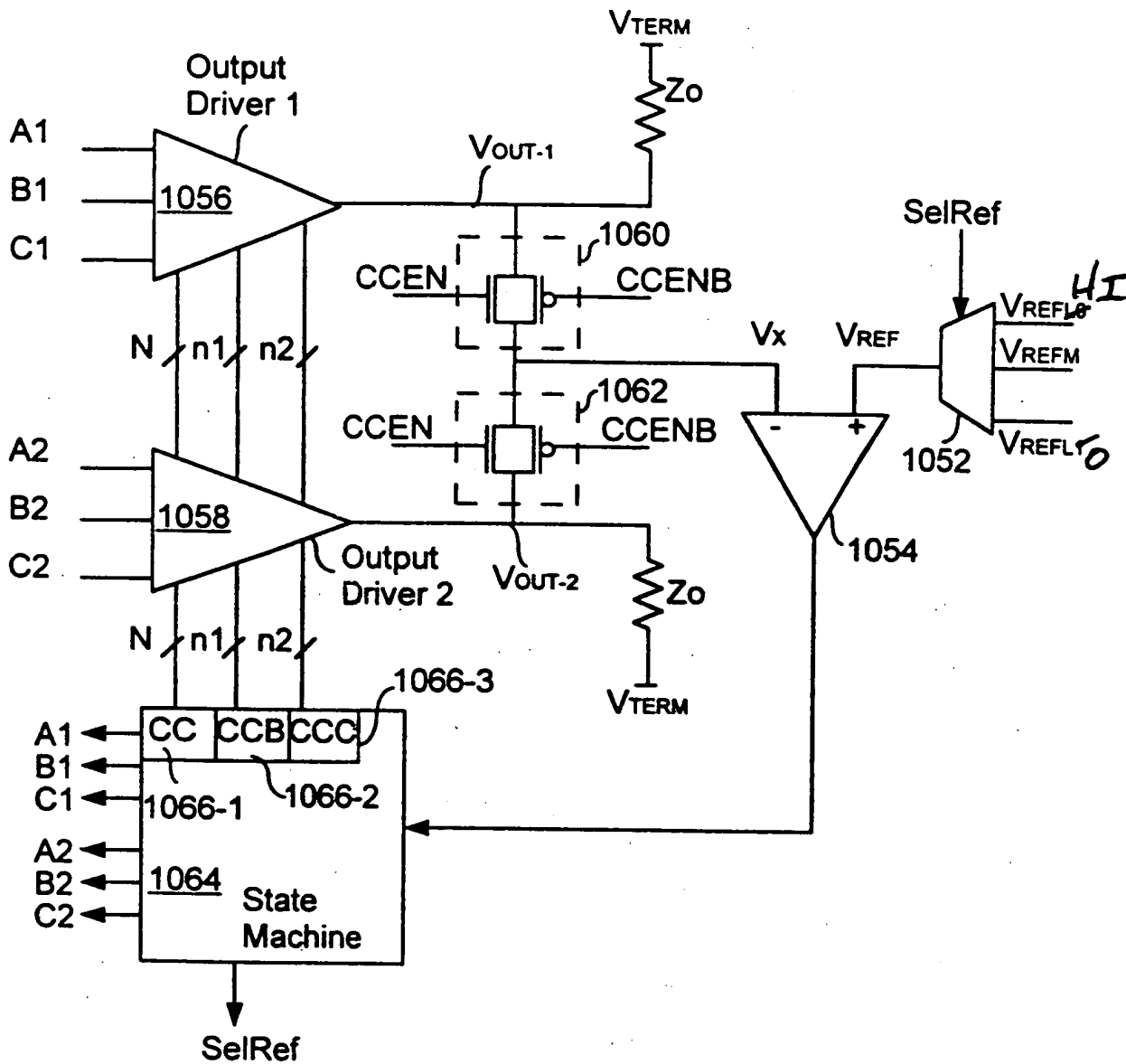


FIG. 9B

1050



Circuit for Calibrating the GDS Compensated Output Driver
with Current Control

FIG. 10

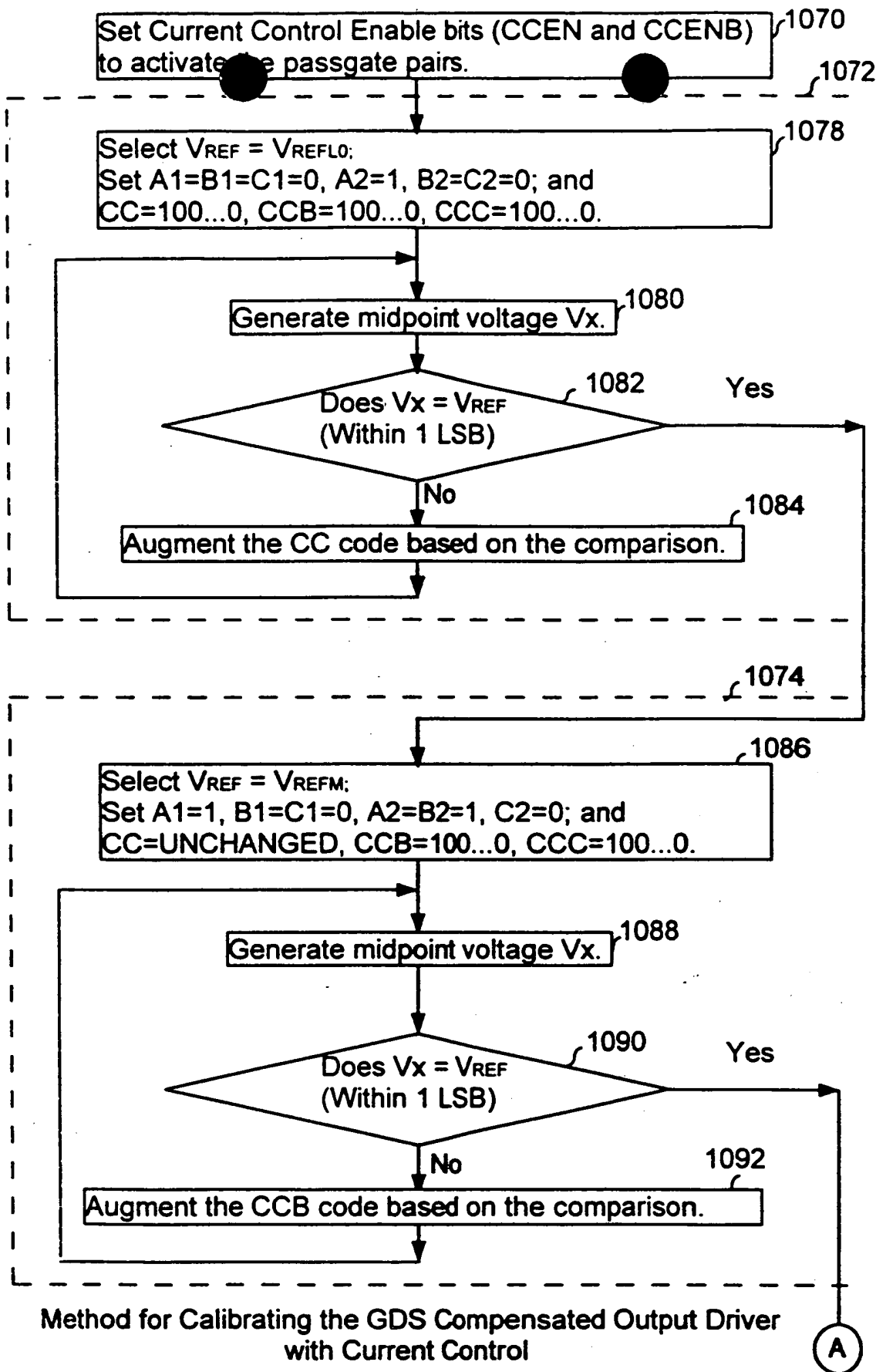
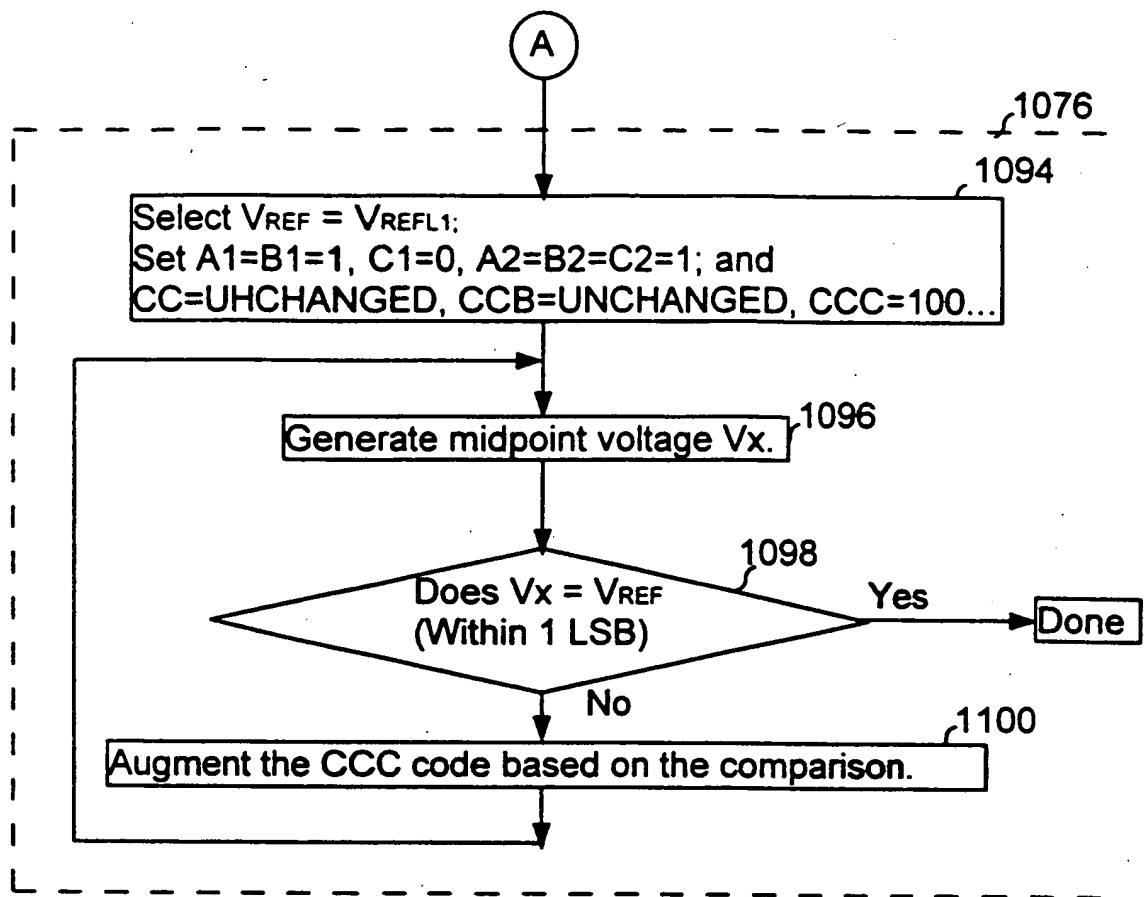


FIG. 11A



Method for Calibrating the GDS Compensated Output Driver
with Current Control

FIG. 118

FIG. 12

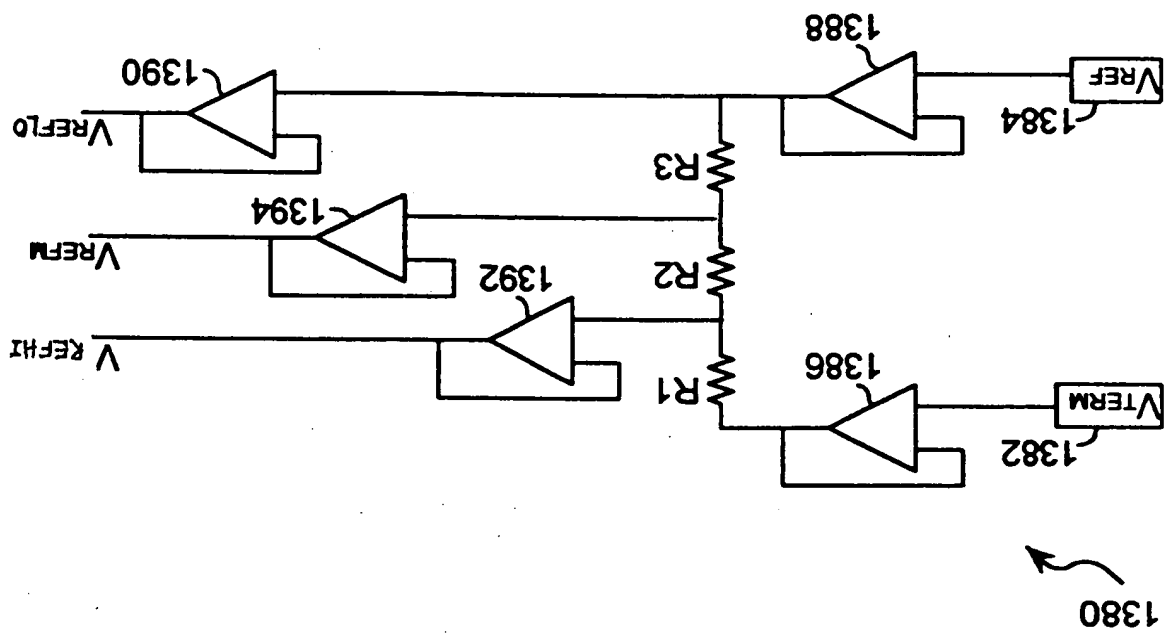
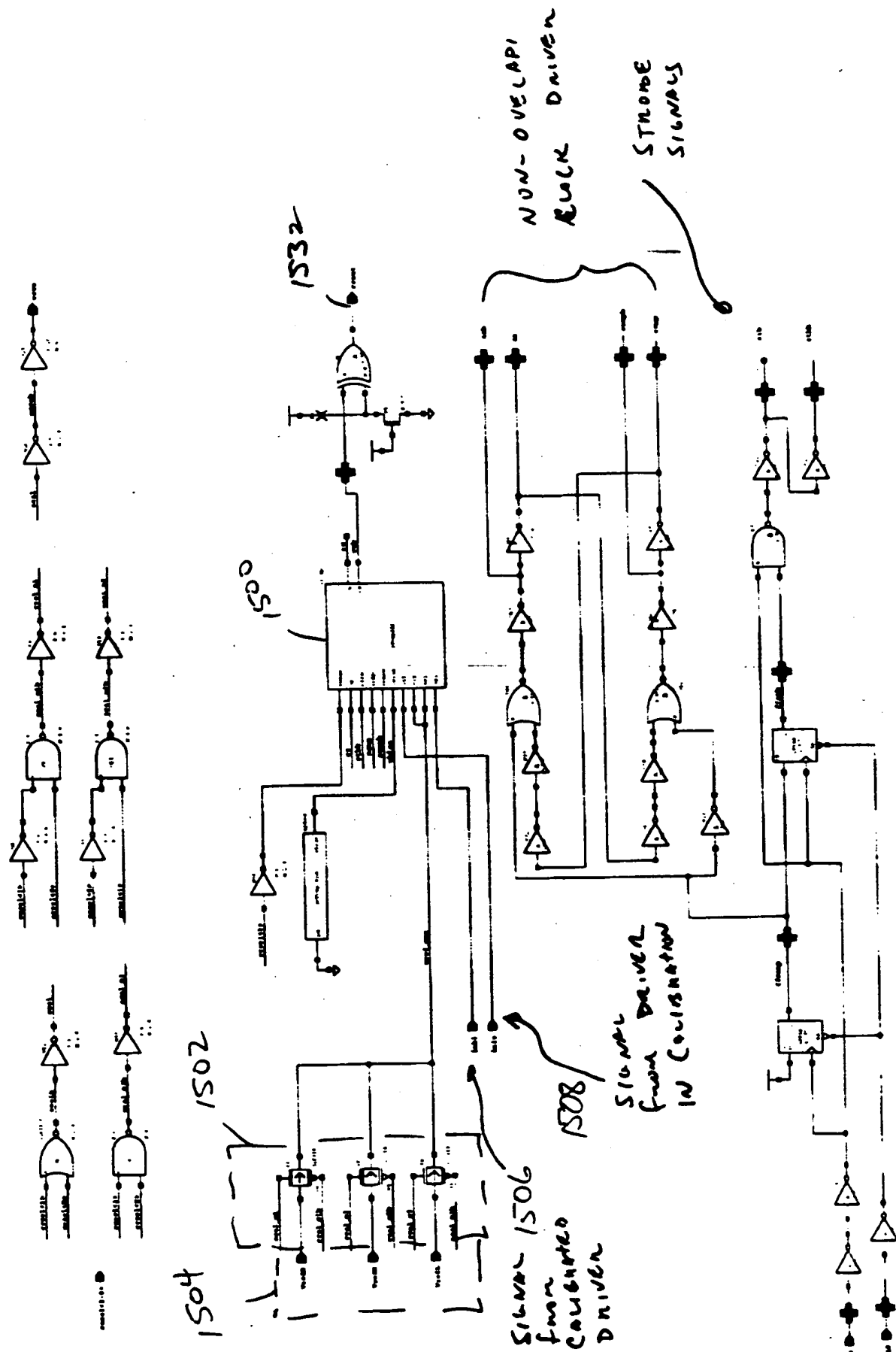


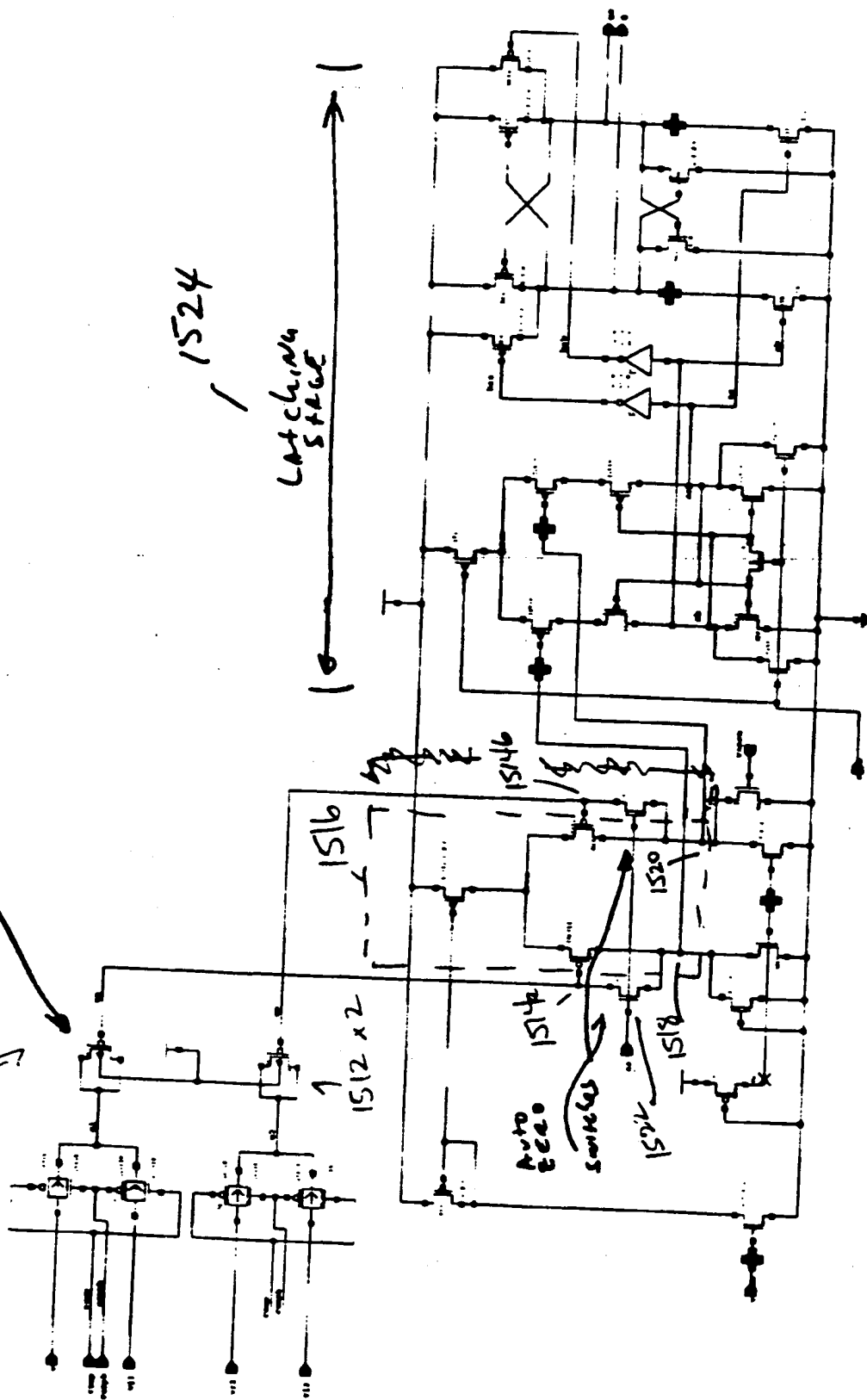
fig 13A



[illegible]

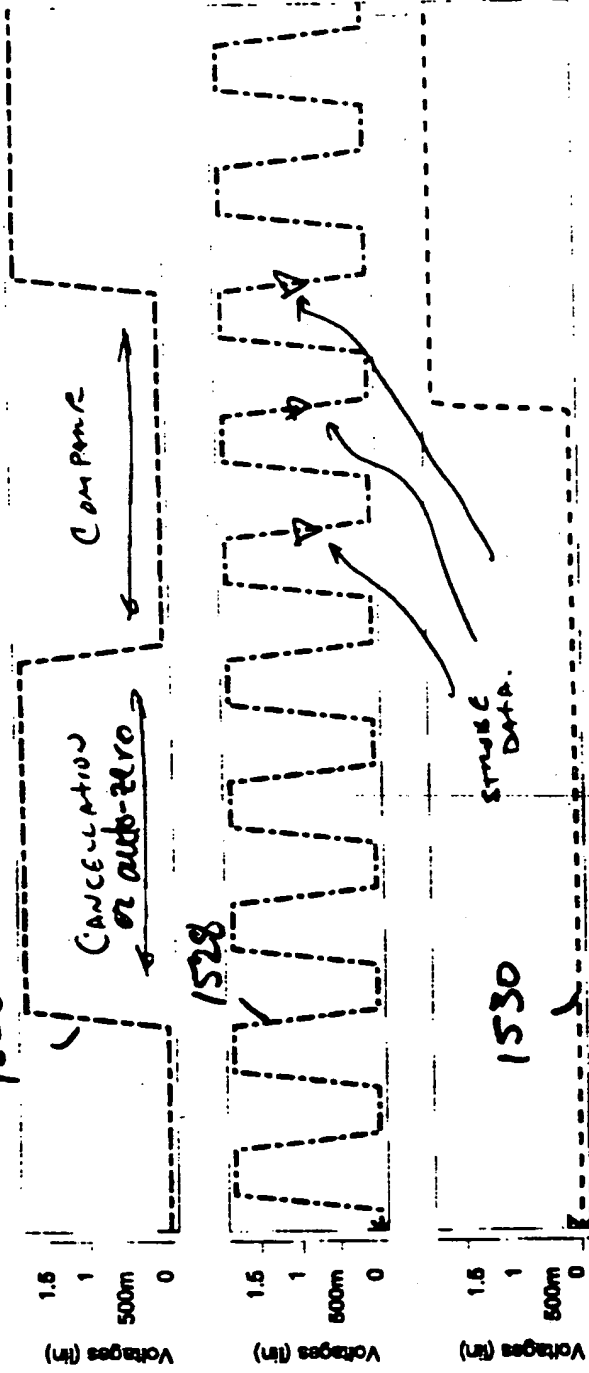
1510 1500

Coupling Cars



cc cal chk

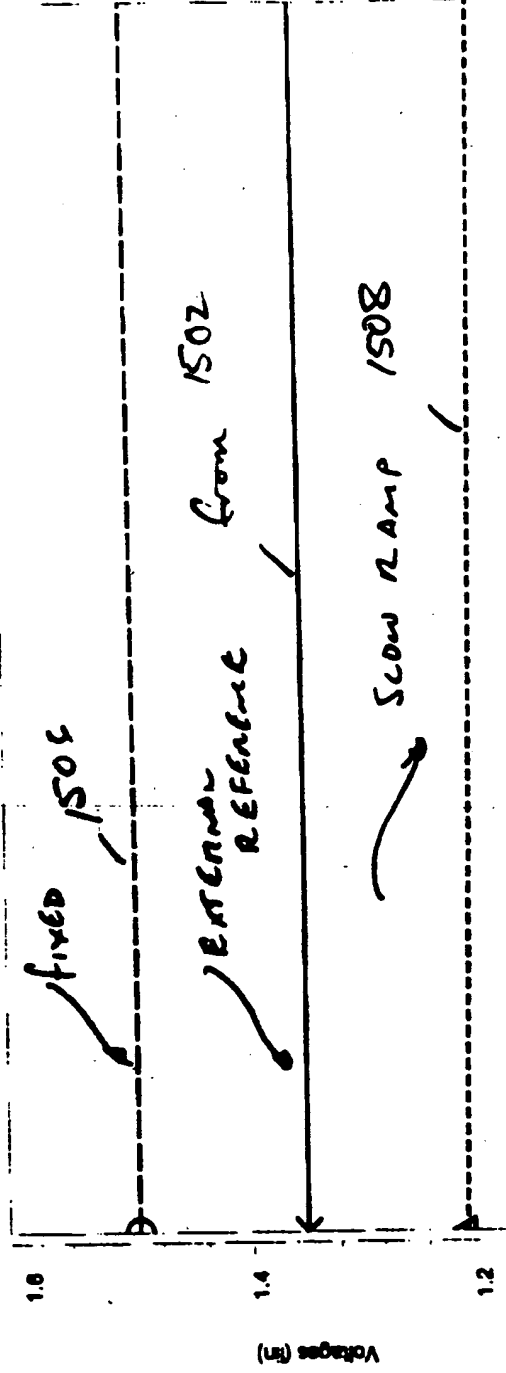
Wave	Symbol
DO:A0-V(ccout)	X
DO:A0-V(ds)	*
DO:A0-V(as)	



2.832u 2.834u 2.836u 2.838u 2.840u 2.842u 2.844u 2.846u 2.848u 2.850u 2.852u 2.854u 2.856u 2.858u
Time (lin) (TIME)

cc cal chk

Wave	Symbol
DO:A0-V(vrefm)	X
DO:A0-V(vrh)	○
DO:A0-V(vrlo)	△



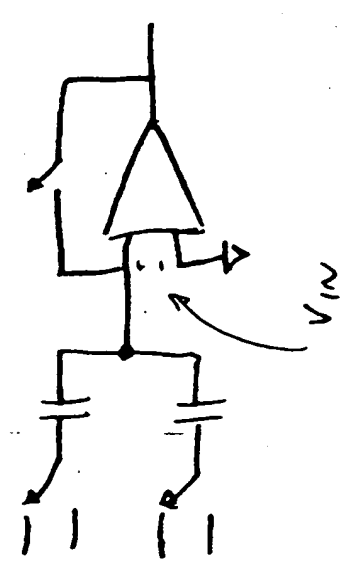
2.832u 2.834u 2.836u 2.838u 2.840u 2.842u 2.844u 2.846u 2.848u 2.850u 2.852u 2.854u 2.856u 2.858u
Time (lin) (TIME)

FIG. 13C

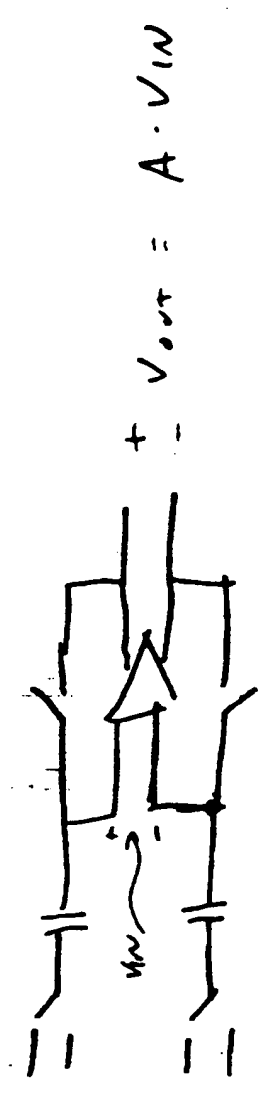
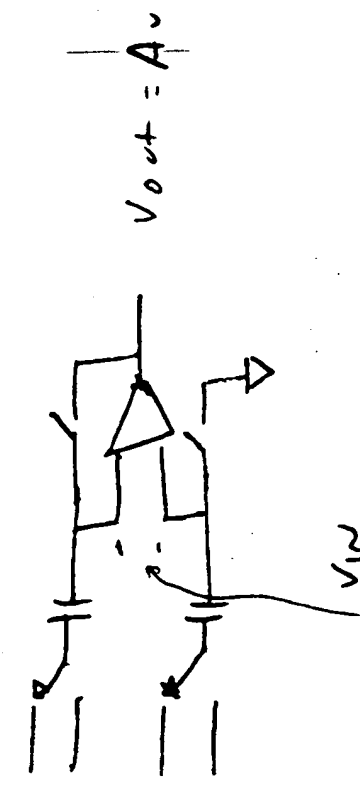
Alternate Implementations

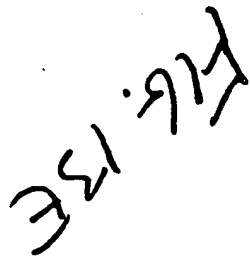
of Differential Amplifier Comparisons

FIG. 13D



Differential IN
Single Ended
OUT





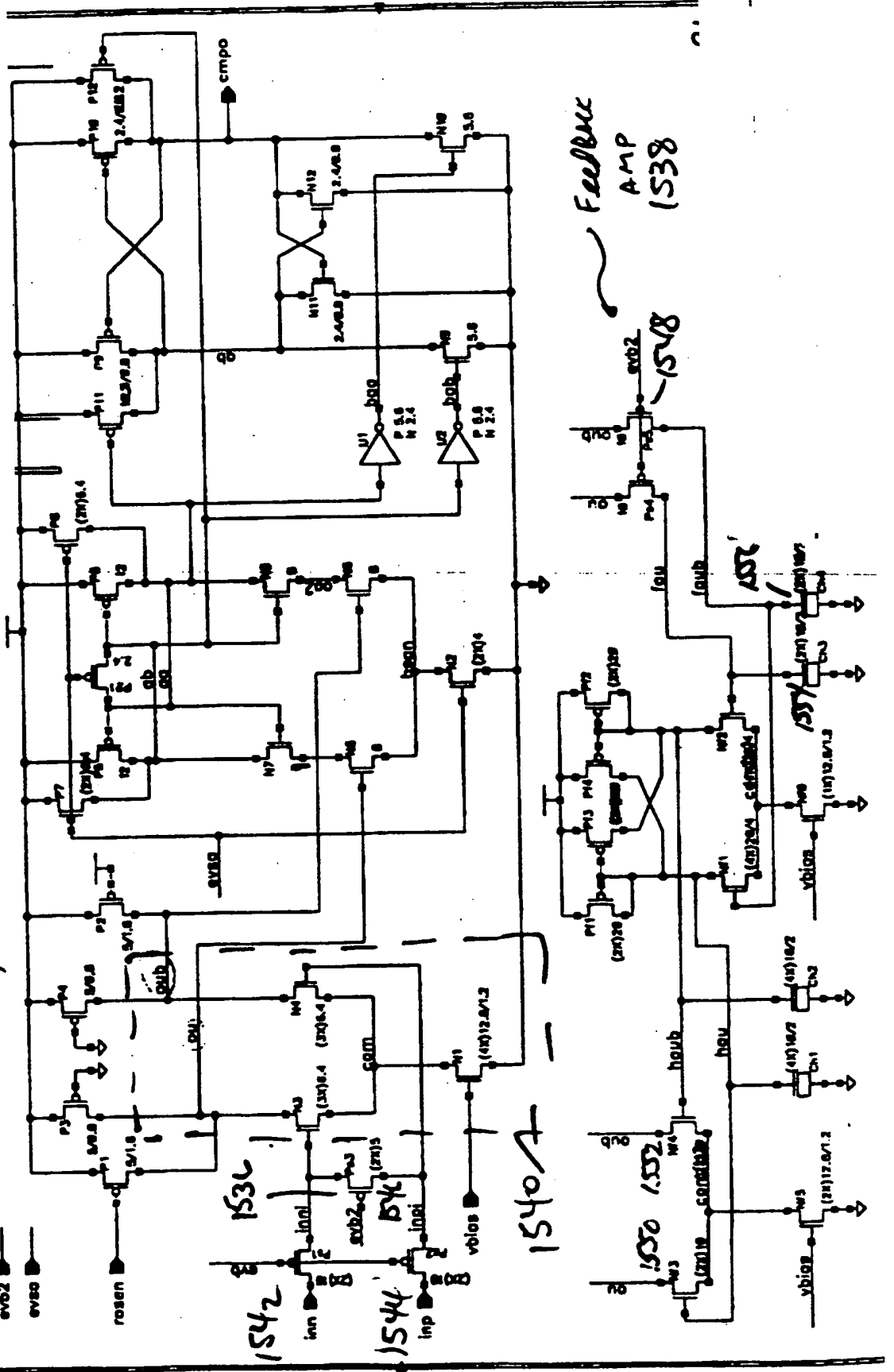
00001-00000000

LA + chura
stage

FIG 14B

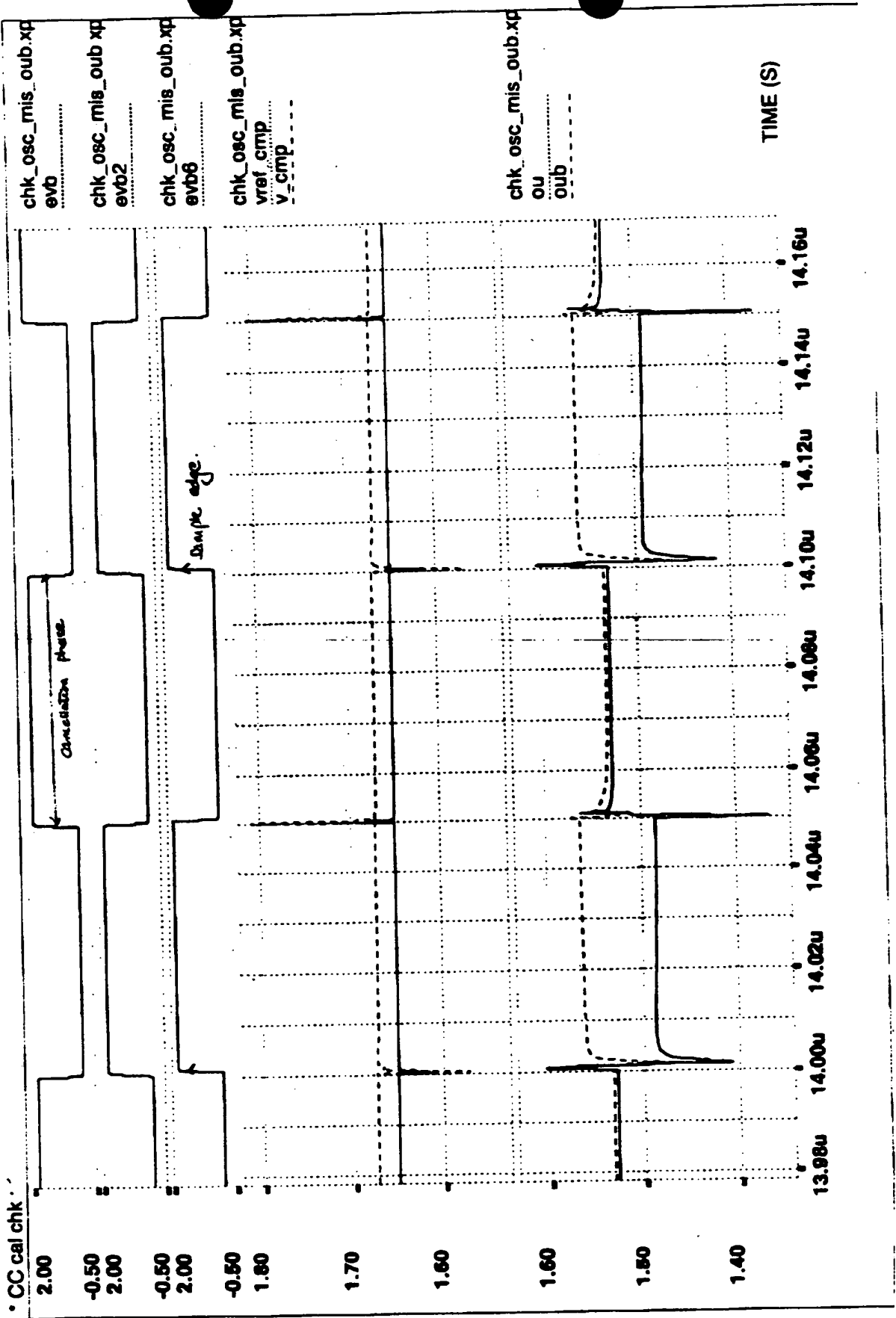
1500

evb
evb2
evb3



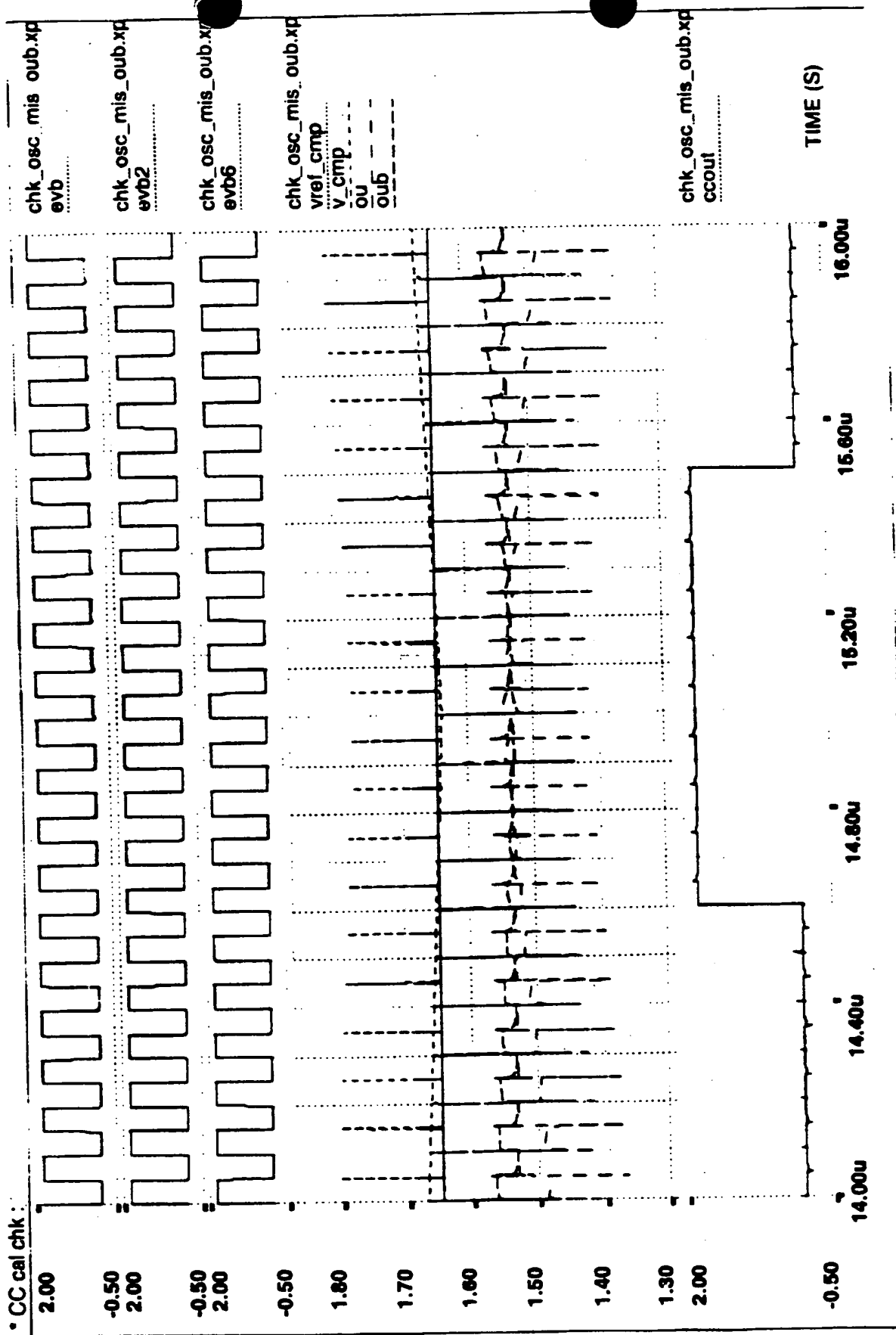
Flb 14C

- CC cal chk.

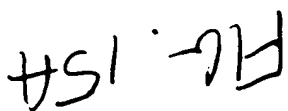


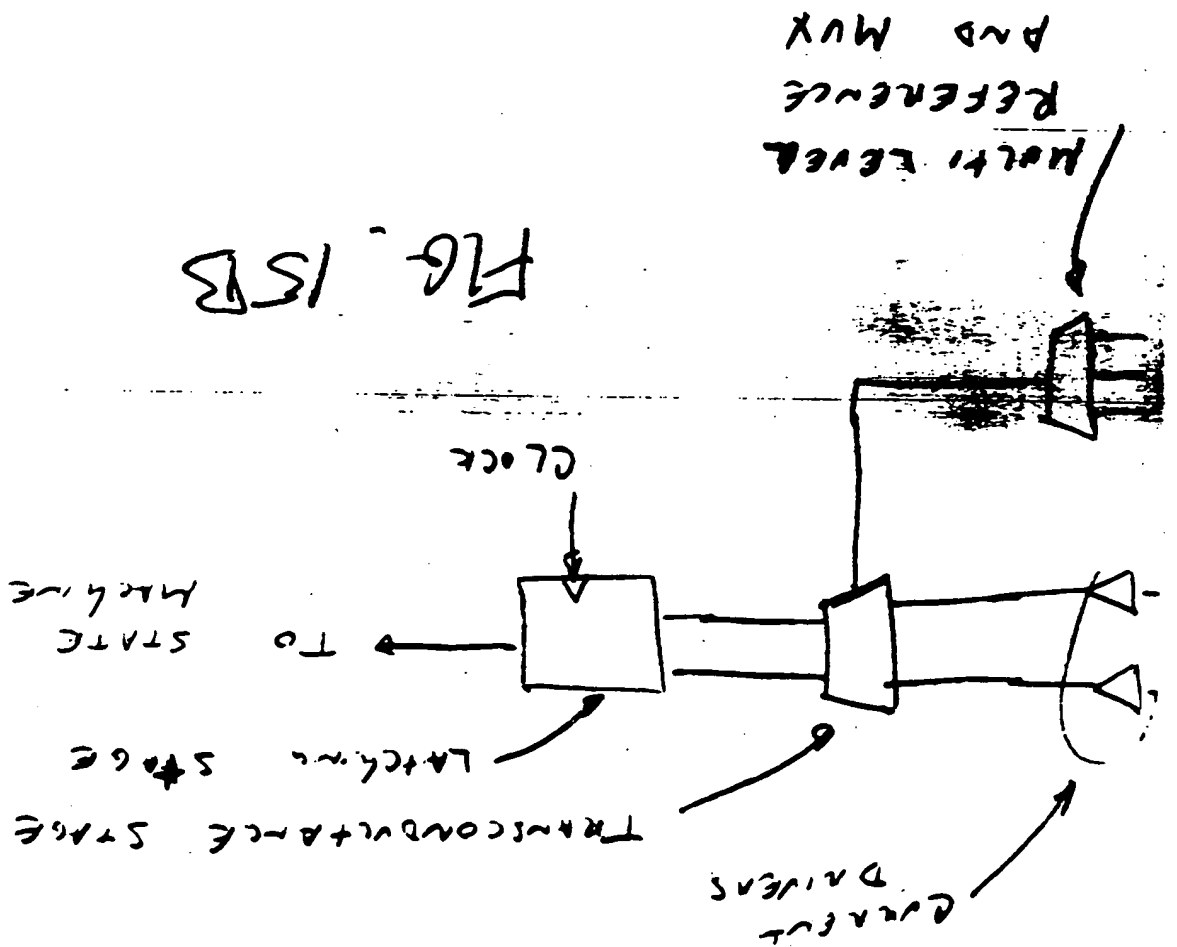
918-142

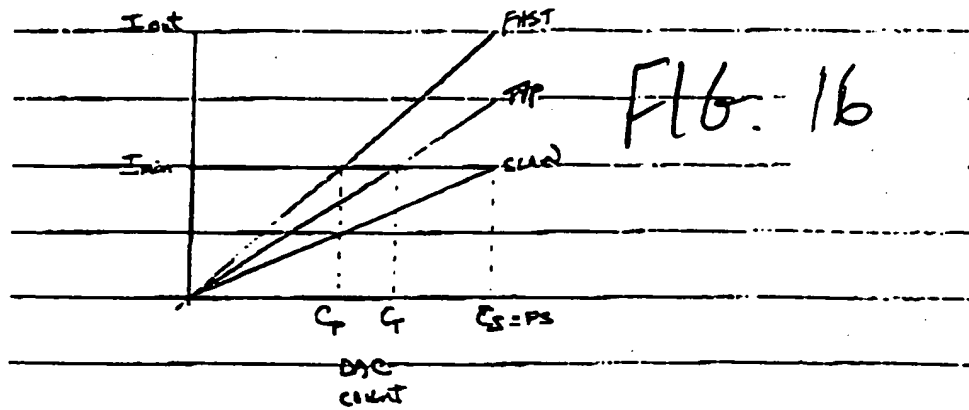
w/ OS Canceled



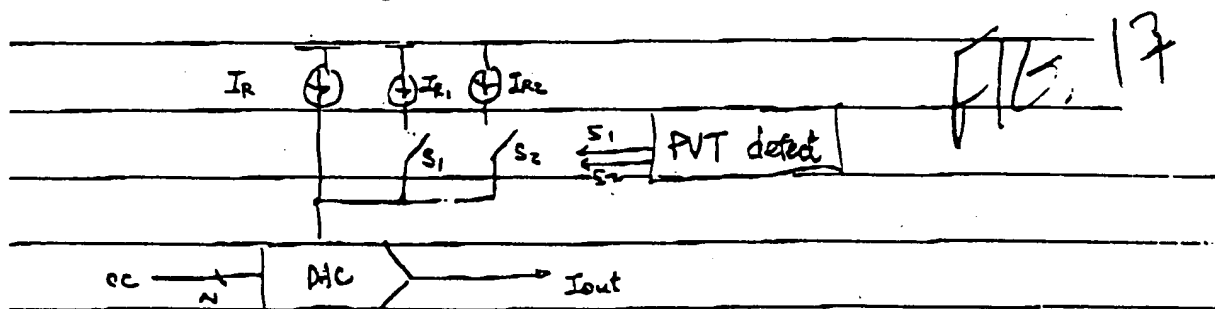
$\frac{1}{2} \sqrt{2}$

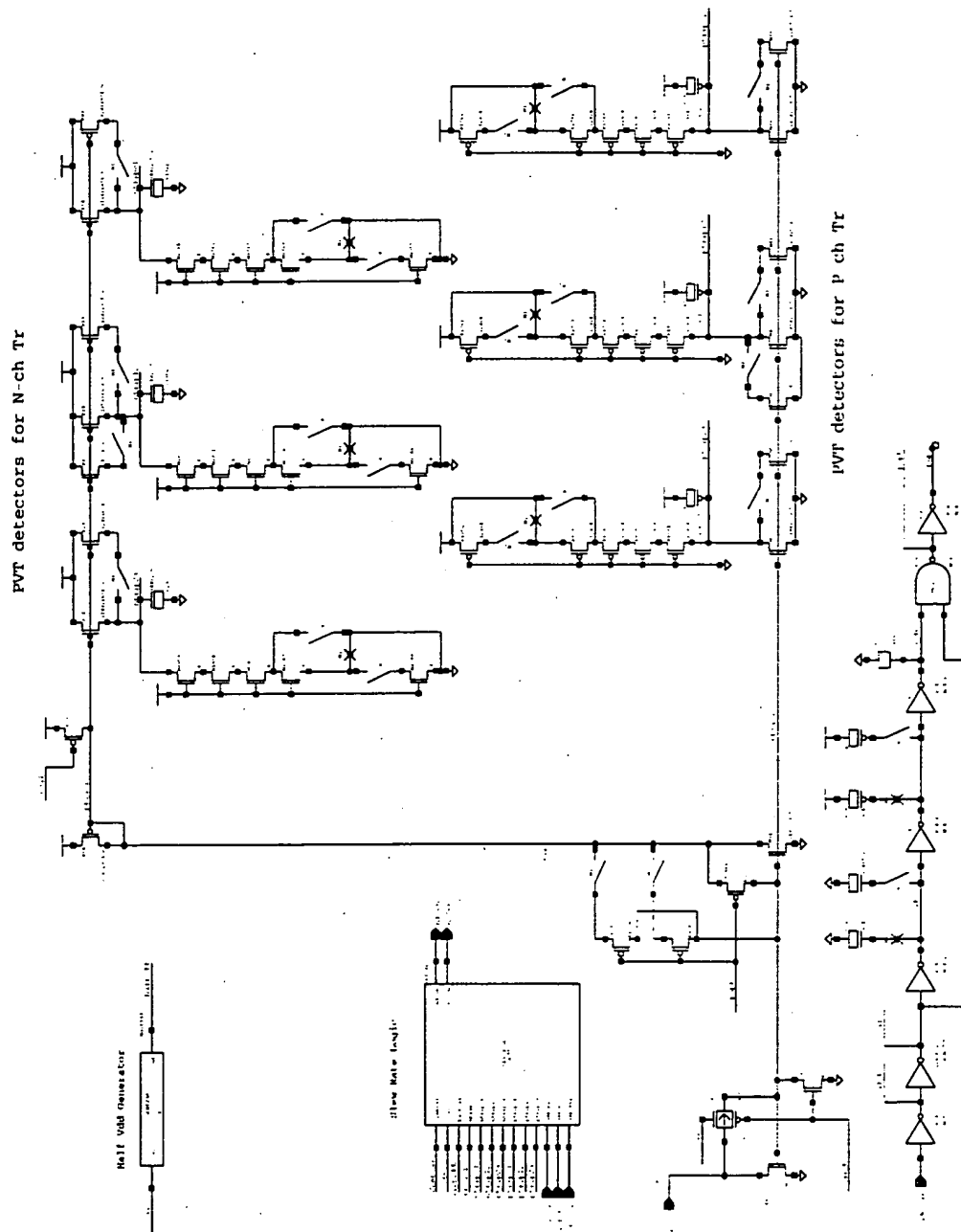






Output driver block diagram:



[illegible]

mpulse

FIG. 19A

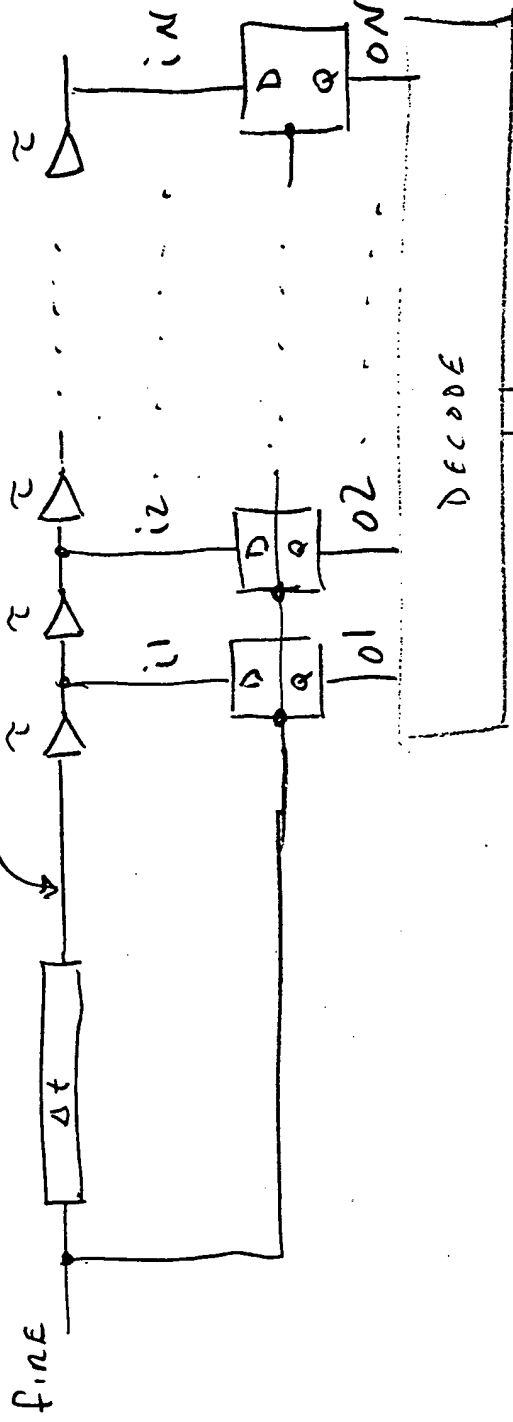
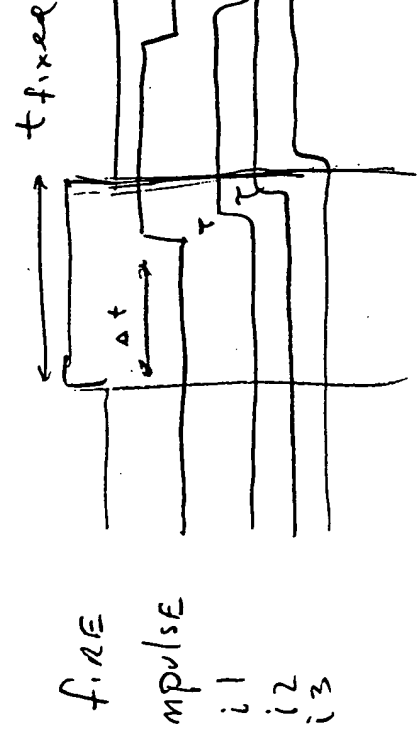


FIG. 19B

slow conditions



DECODED outputs

fast conditions

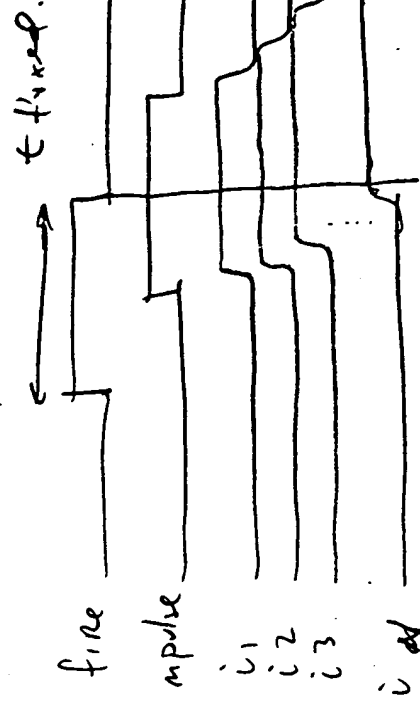
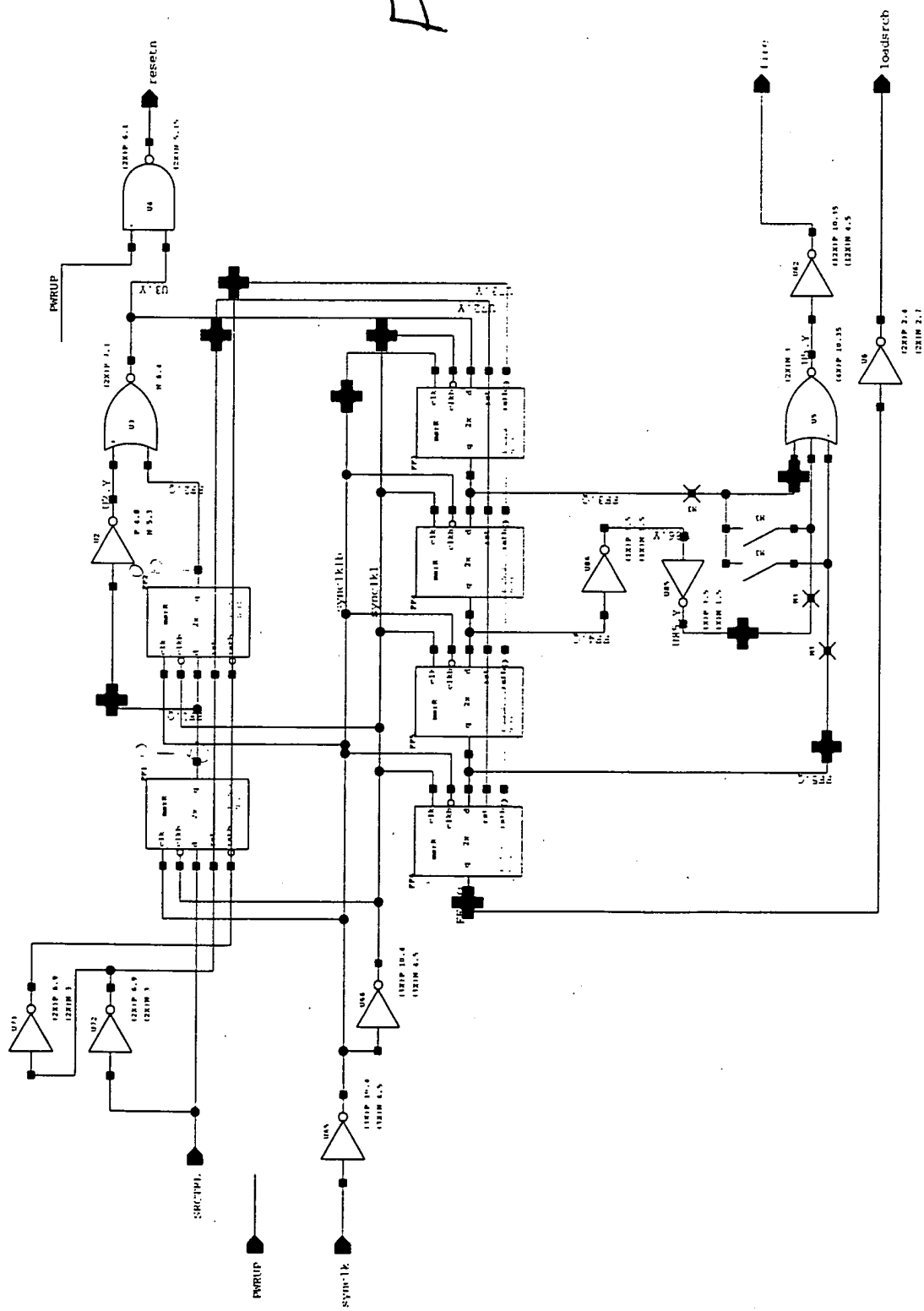


FIG-20B



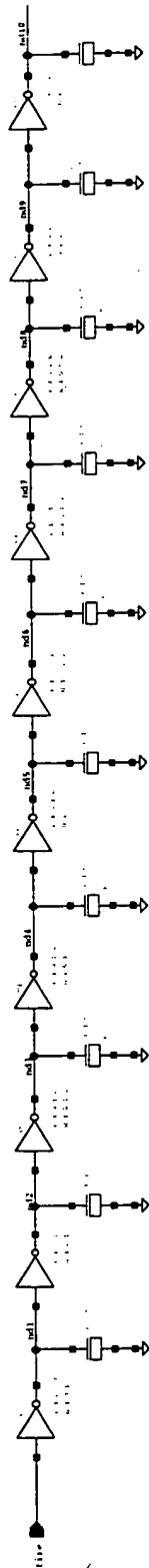
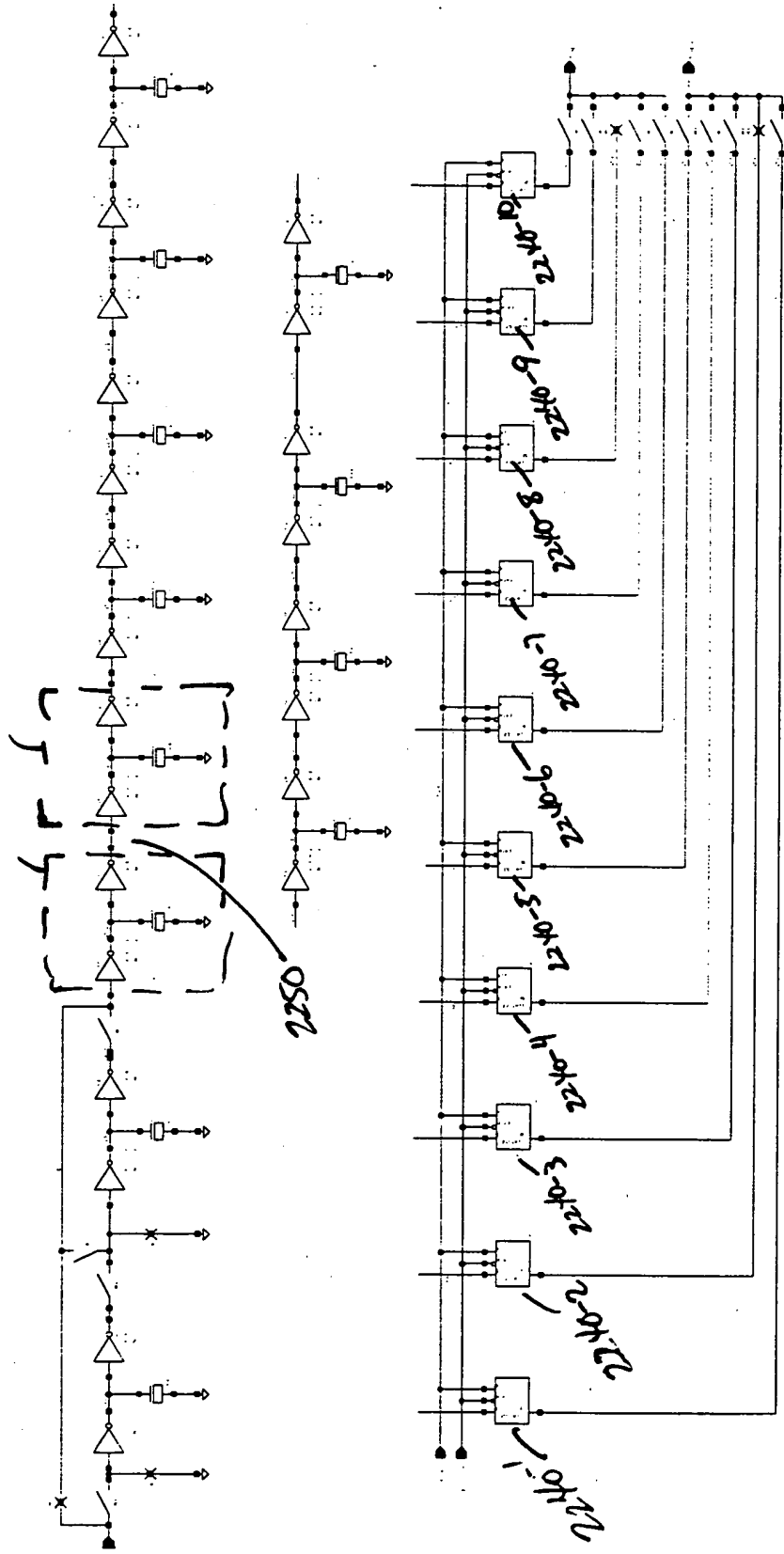
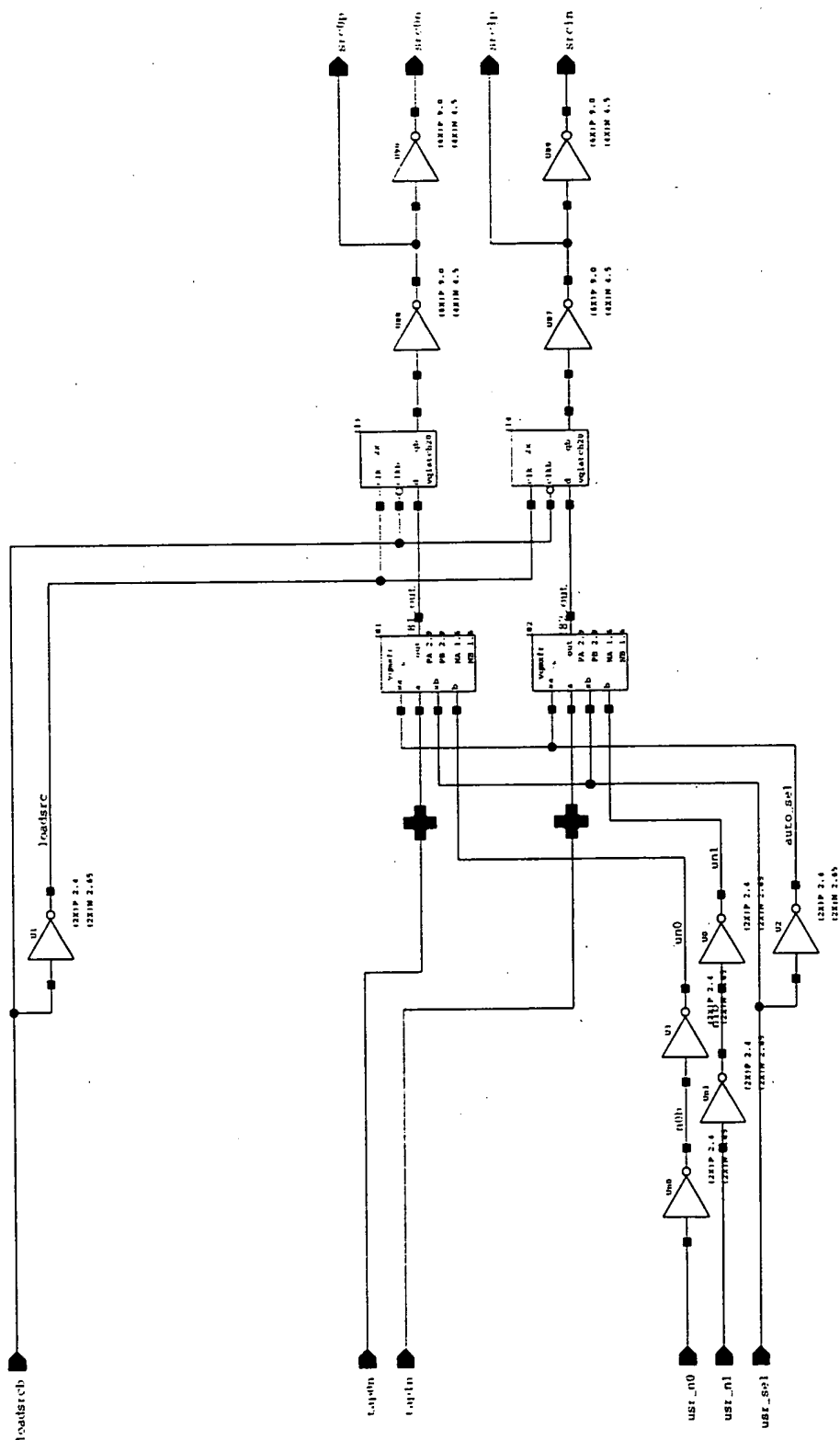
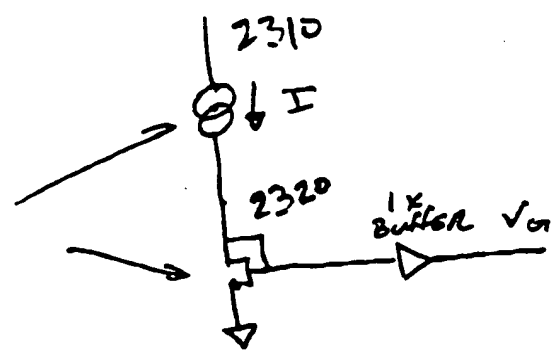
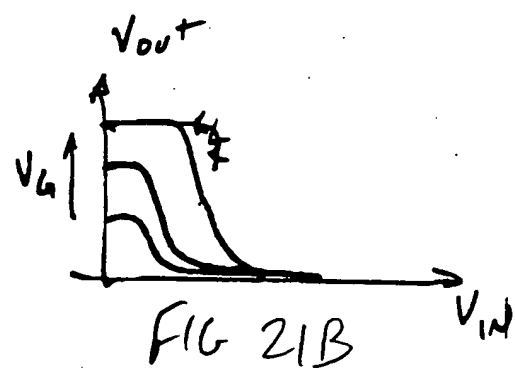
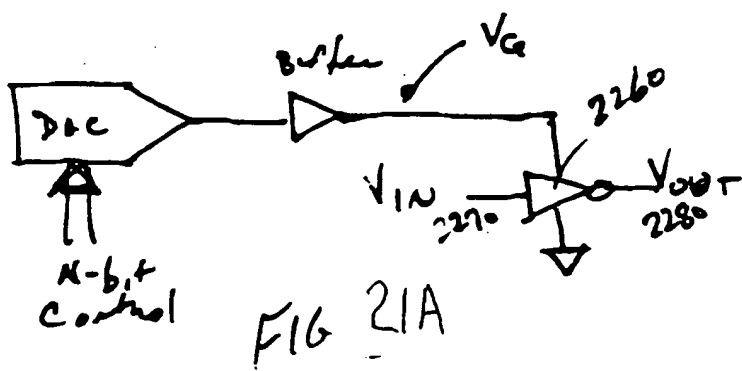


Fig. 20D

2230¹ 2230² ...







ALTERNATE EMBODIMENT of V_{gate} DAC

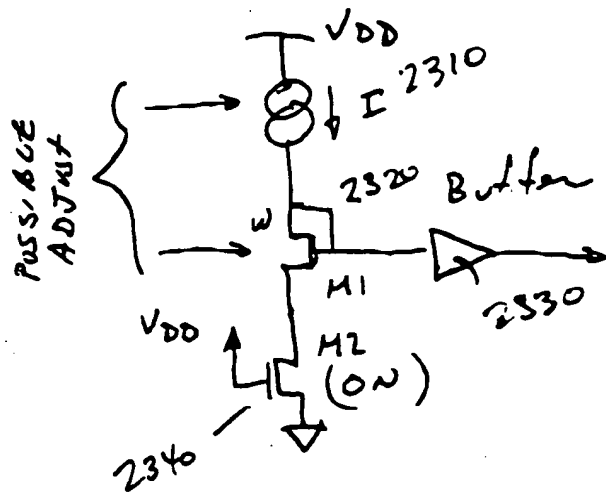


Fig. 23

M1, M2 scaled to match actual output driver.


```

- vref
+ vref_foxx

```

76-24B

